FIG. 1 BACKGROUND ART

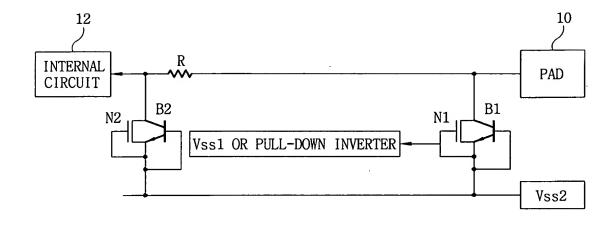


FIG.2
BACKGROUND ART 10 12 PAD INTERNAL CIRCUIT Vss1 OR PULL-DOWN INVERTER Vss2 N1 N2  $p^{+}$ n<sup>+</sup> n<sup>+</sup> n<sup>+</sup> p<sup>+</sup> n<sup>+</sup> p<sup>+</sup> SUBSTRATE

FIG.3

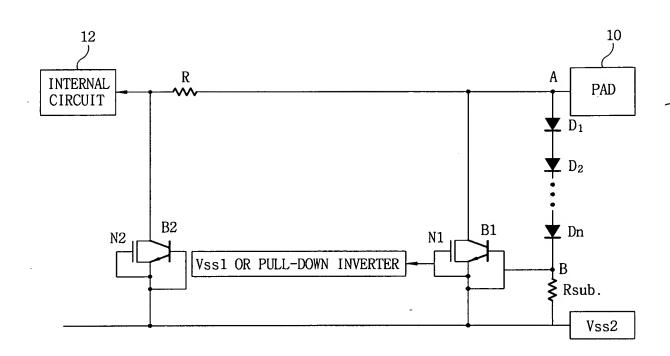


FIG.4

